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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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Sheet	1	of	2
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**Complete if Known**

Application Number	10/661,181
Filing Date	09/11/2003
First Named Inventor	Daniel W. Wong
Art Unit	2173
Examiner Name	na
Attorney Docket Number	00100.02.0056

## U. S. PATENT DOCUMENTS

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## FOREIGN PATENT DOCUMENTS

<b>FOREIGN PATENT DOCUMENTS</b>						
<b>Examiner Initials*</b>	<b>Cite No.<sup>1</sup></b>	<b>Foreign Patent Document</b>	<b>Publication Date MM-DD-YYYY</b>	<b>Name of Patentee or Applicant of Cited Document</b>	<b>Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear</b>	<b>T<sup>6</sup></b>
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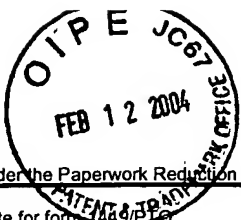
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Sheet

2

of

2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		YOSHIDA, Junko; "Philips new chip said to remove LCD TV artifacts"; EE Times; July 8, 2003; pages 1-2; www.eetimes.com/sys/news/OEG20030708S0017.	

Examiner Signature		Date Considered	
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